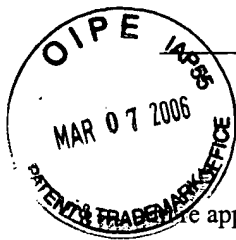


C 25

Patent No.: 6,987,298

PATENT  
Docket No. JCLA13077  
page 1



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re application of :  
Patent No. : 6,987,298 B2  
Issue Date : January 17, 2006  
Application No. : 10/823,488  
Filed : April 12, 2004  
For : CIRCUIT LAYOUT AND STRUCTURE FOR A  
NON-VOLATILE MEMORY  
  
Examiner : HUYNH, ANDY  
Art Unit : 2818

Certificate of Mailing

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

March 2, 2006

(Date)

Jawei Huang, Reg. No. 43,330

**TRANSMITTAL SHEET**

**MAIL STOP Certificate of Correction**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Certificate**

**MAR 13 2006**

**of Correction**

Sir:

Transmitted herewith is a **Petition Request for Corrected Patent Grant Under 37 CFR 1.323**.

Also enclosed are:

- (X) Certificate of Correction.
- (X) A check in amount of \$ 100 to cover the petition fee.
- (X) Return prepaid postcard.
- (X) Commissioner is authorized to charge any fees required in connection with the filing of this paper to account No. 50-0710 (Order No. JCLA13077).

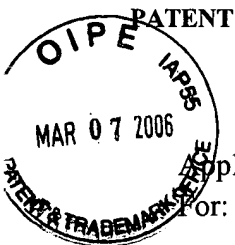
Date: 3/2/2006

**Correspondence Address:**

J.C. Patents  
4 Venture, Suite 250  
Irvine, California 92618  
TEL: (949) 660-0761

Jiawei Huang  
Registration No. 43,330

**MAR 15 2006**



Attorney Docket No.: JCLA13077

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: CHIEN-HSING LEE et al. )  
For: CIRCUIT LAYOUT AND STRUCTURE FOR )  
A NON-VOLATILE MEMORY )  
Filing Date: April 12, 2004 )  
Application No.: 10/823,488 )  
Issue Date: January 17, 2006 )  
Patent No.: 6,987,298 B2 )

**REQUEST FOR CERTIFICATE OF CORRECTION**  
**Under 37 CFR 1.323**

**MAIL STOP Certificates of Correction**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

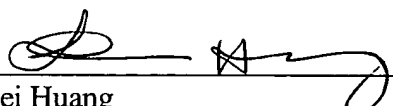
Enclosed for filing under 37 C.F.R. 1.323 is a Certificate of Correction in duplicate in connection with the above-identified patent.

Applicant respectfully requests that the Patent Grant of the above-identified application be corrected to reflect the assignee's true name, "Solid State System Co., Ltd.". Currently, the Patent Grant incorrectly shows the name of the assignee as "Solide State System Co., Ltd.". The mistake to be corrected was made by the applicant and is of clerical nature, and the correction does not constitute new matter or require reexamination.

A check in the amount of \$100 is enclosed herewith to cover the fee set forth in 37 C.F.R. 1.20(a).

Respectfully submitted,  
J.C. PATENTS

Dated: 3/2/2006

By:   
Jiawei Huang  
Registration No. 43,330

4 Venture, Suite 250  
Irvine, CA 92618  
Tel.: (949) 660-0761

MAR 15 2006

PTO/SB/44 (02-01)

Approved for use through 01/31/2004. OMB 0651-0033

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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(Also Form PTO-1050)

**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

PATENT NO : 6,987,298 *B2*  
DATED : January 17, 2006  
INVENTOR(S) : CHIEN-HSING LEE et al.

It is certified that error appears in the above-identified patent and that said Letters Patent  
is hereby corrected as shown below:

Title Page.

Please replace item [73], Assignee's name from Solide State System Co., Ltd. to –  
Solid State System Co., Ltd. --

MAILING ADDRESS OF SENDER:

**4 Venture, Suite 250  
Irvine, CA 92618**

PATENT NO. **6,987,298**

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**MAR 15 2006**